



孕龍科技股份有限公司
Zeroplus Technology Co., Ltd.

SPECIFICATION

MODEL: B08021-LAP-PCI-M

PART NO : _____

VERSION : V1.21

Approver		Check	Design
GM	PM		

Customer Confirm

* Please fax the file to Zeroplus Technology after signing.

2F, NO.123, Jian Ba Rd,
Chung Ho City, Taipei Hsian, R.O.C.

Tel:+886-2-66202225
Fax:+886-2-22234362



Content

1	Software Register	3
2	User Interface	6
3	Operating Instructions	9



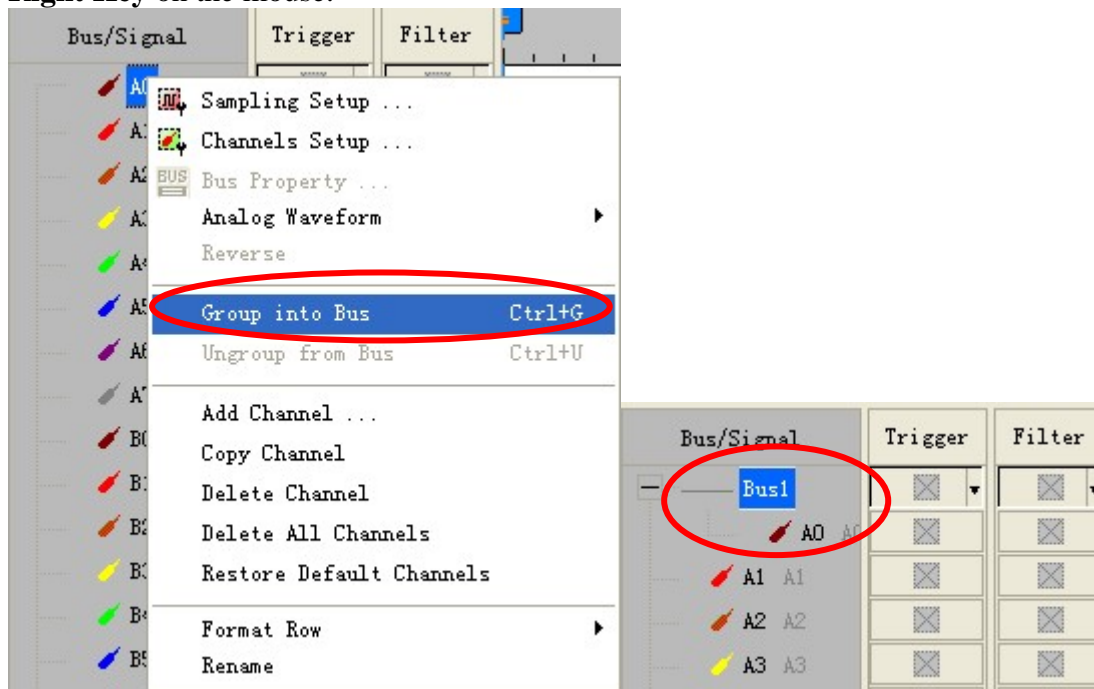
1 Software Register

Please register the software as the following steps:

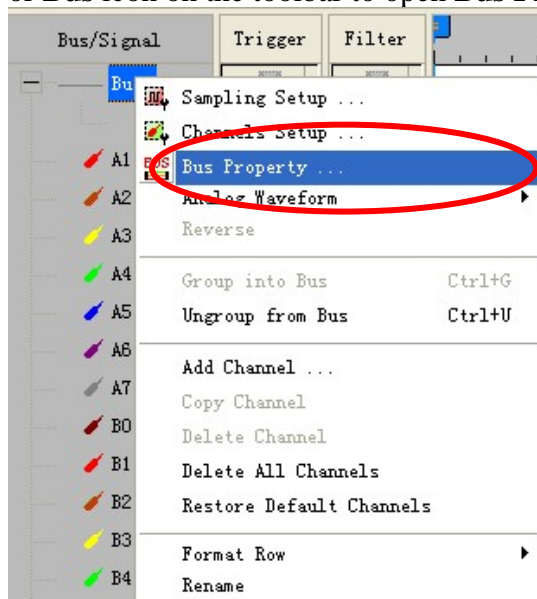
※ Remark1: The registration steps for all protocol analyzers are the same; you can complete the registration by following procedures. Following is an example on how to register the Protocol Analyzer BUS.

※ Remark2: We won't have additional notice for you, when there is any modification of the module specification. If there is some unconformity caused by the module version upgrade, users should take the module software as the standard.

STEP 1. Open the Logic Analyzer and group the unanalyzed channels into **Bus1** by pressing the **Right Key** on the mouse.

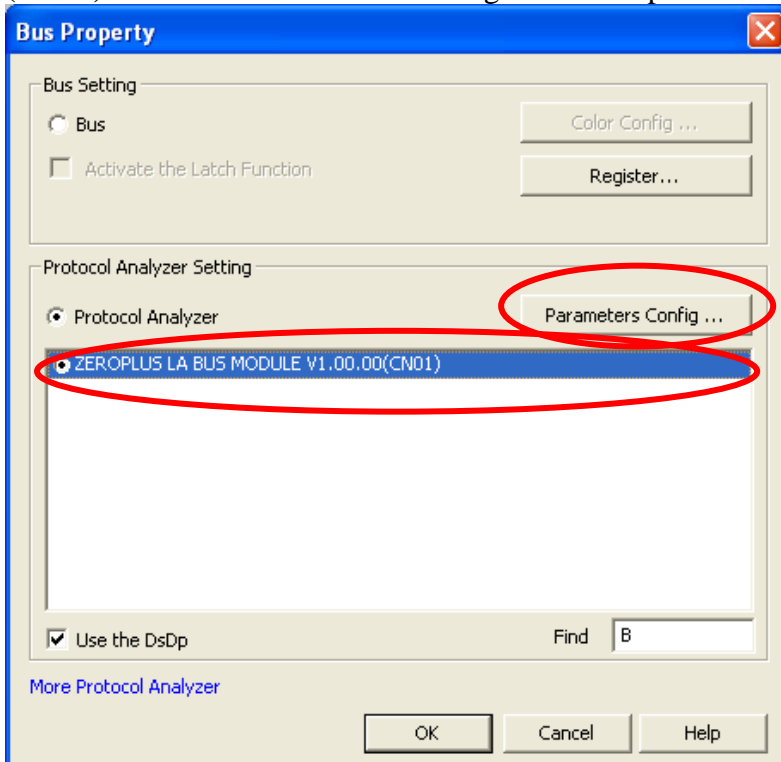


STEP 2. Select **Bus 1**, then press **Right Key** on the mouse to list the menu, then press **Bus Property** or **Bus** icon on the toolbar to open **Bus Property** dialog box.

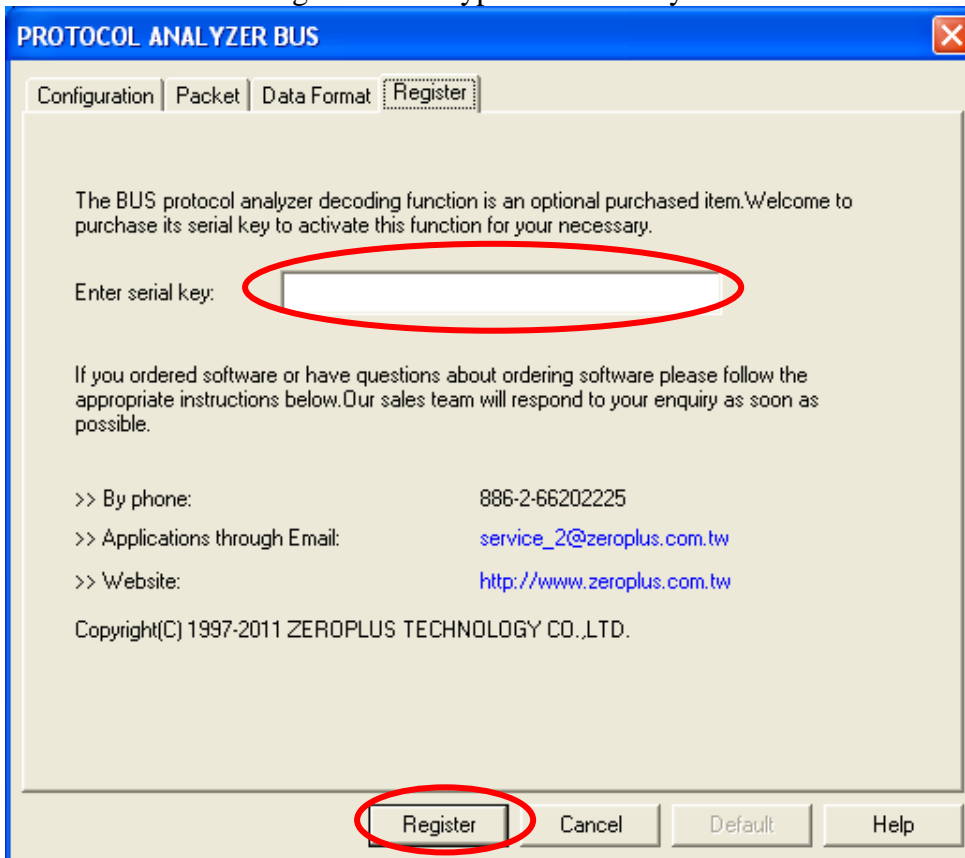




STEP 3. Select the Protocol Analyzer, and then choose **ZEROPLUS LA BUS MODULE V1.00.00 (CN01)**. Next click Parameters Configuration to open Protocol Analyzer Bus dialog box.

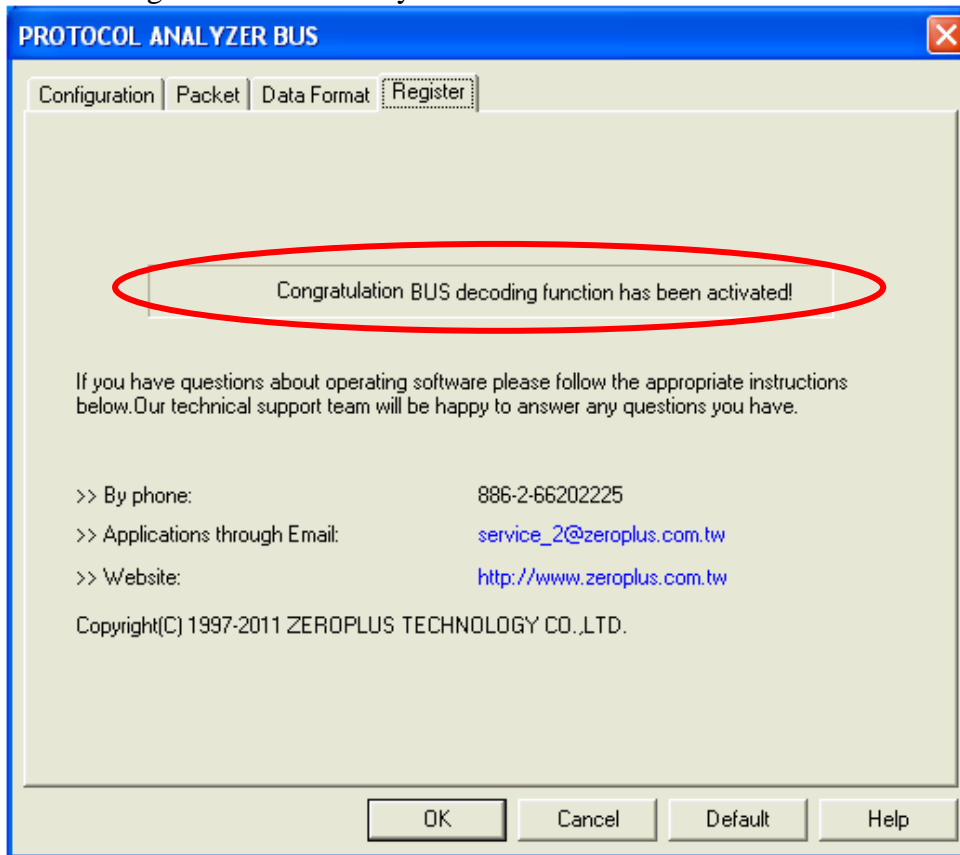


STEP 4. Press Register tab to type the serial key number of BUS. Then press Register.





STEP 5. After pressing the Register button, following dialog box will appear, it denotes that the BUS has been registered successfully.

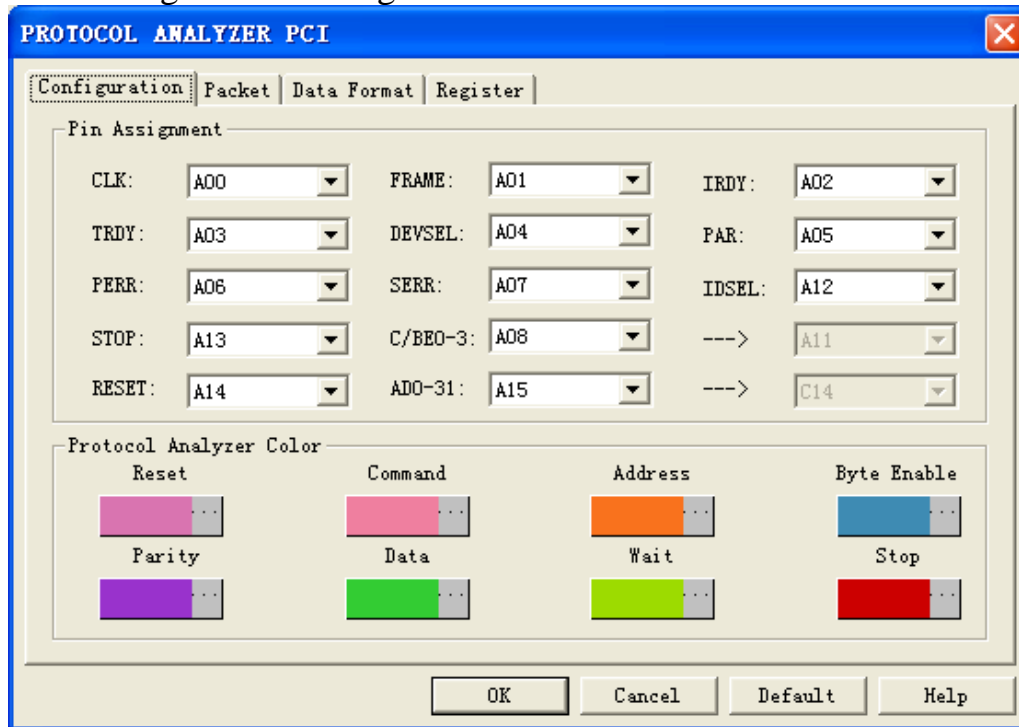




2 User Interface

In the configuration dialog box, please refer to the below images to select options of setting **PCI MODULE**.

PCI Configuration Dialog Box



Pin Assignment: There are 47 channels.

CLK: It is the transmission channel of the clock signal; its default is A00.

FRAME: It is the transmission channel of the frame period signal; its default is A01.

IRDY: It is the transmission channel which denotes whether the host device signal has been ready; its default is A02.

TRDY: It is the transmission channel which denotes whether the slave device signal has been ready; its default is A03.

DEVSEL: It is the transmission channel of the device address signal; its default is A04

PAR: It is the transmission channel of the data even parity signal; its default is A05.

PERR: It is the transmission channel which reports the even parity error; its default is A06.

SERR: It is the transmission channel which reports the system error; its default is A07.

IDSEL: It is the transmission channel of Initialization Device Select which is used as a chip select during the read and write operation of the configuration room; its default is A12.

STOP: It is the transmission channel of stopping the data transmission signal; its default is A13.

RESET: It is the transmission channel of resetting the asynchronous signal; its default is A14.

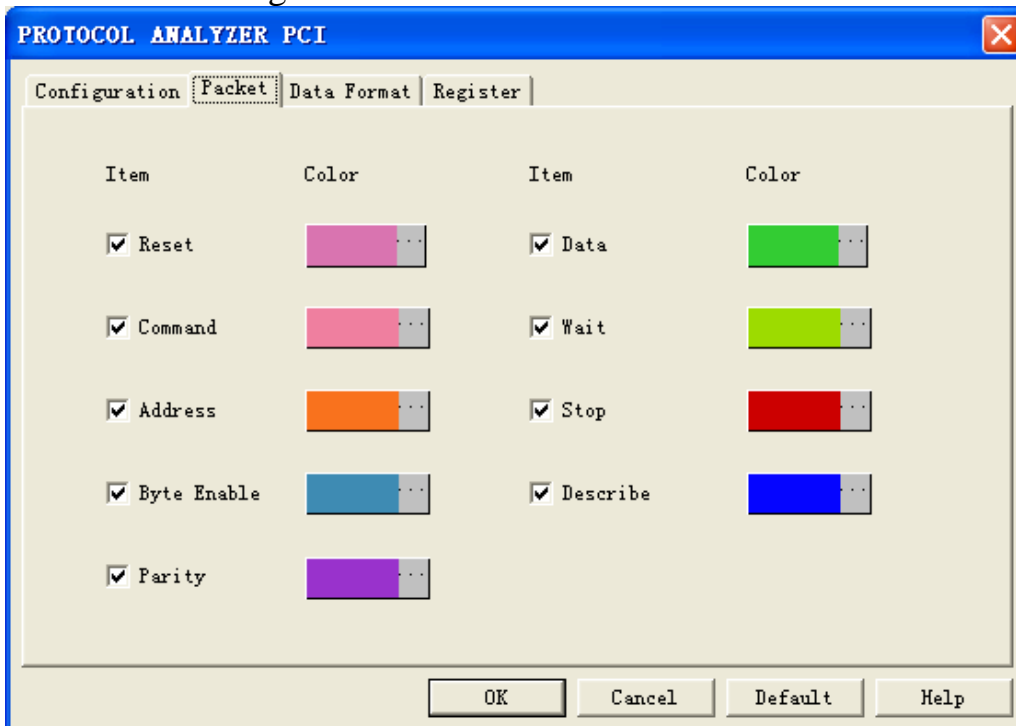
C/BE0-3: It consists of 4 consecutive channels; the default is A08 (It includes A08 and the next three consecutive channels). It is the transmission channel of Command.

AD0-31: It consists of 32 consecutive channels; the default is A15 (It includes A15 and the next thirty-one consecutive channels). It is the transmission channel of Address or Data.

Protocol Analyzer Color: Users can vary the color of the items according to their requirements.

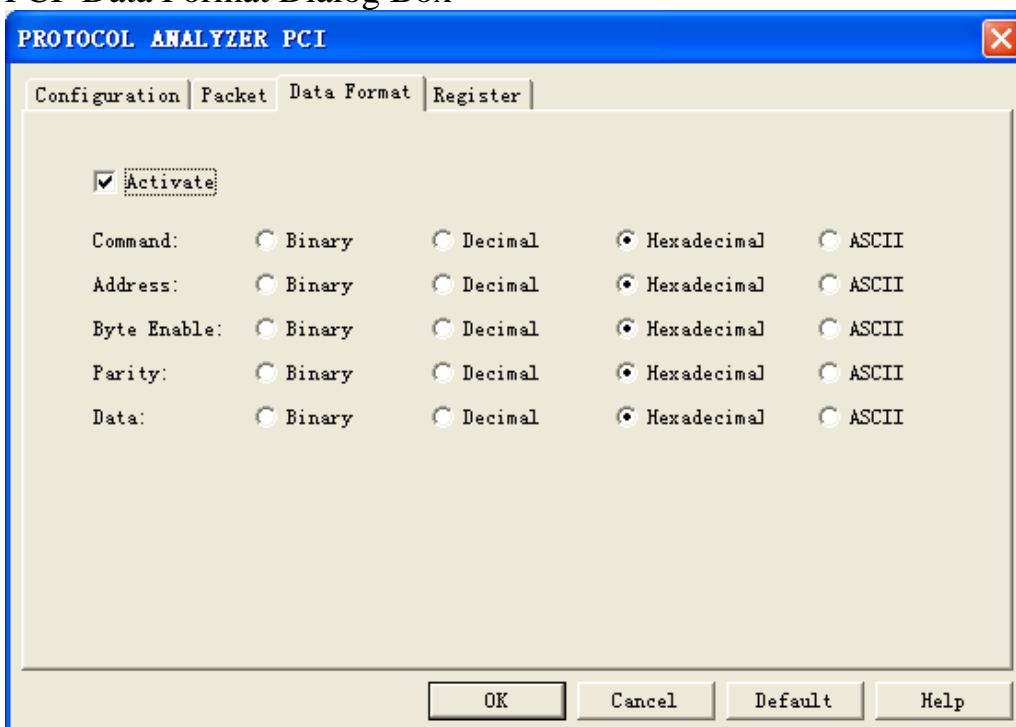


PCI Packet Dialog Box



In the Packet part, users can set the items and colors as users' requirements.

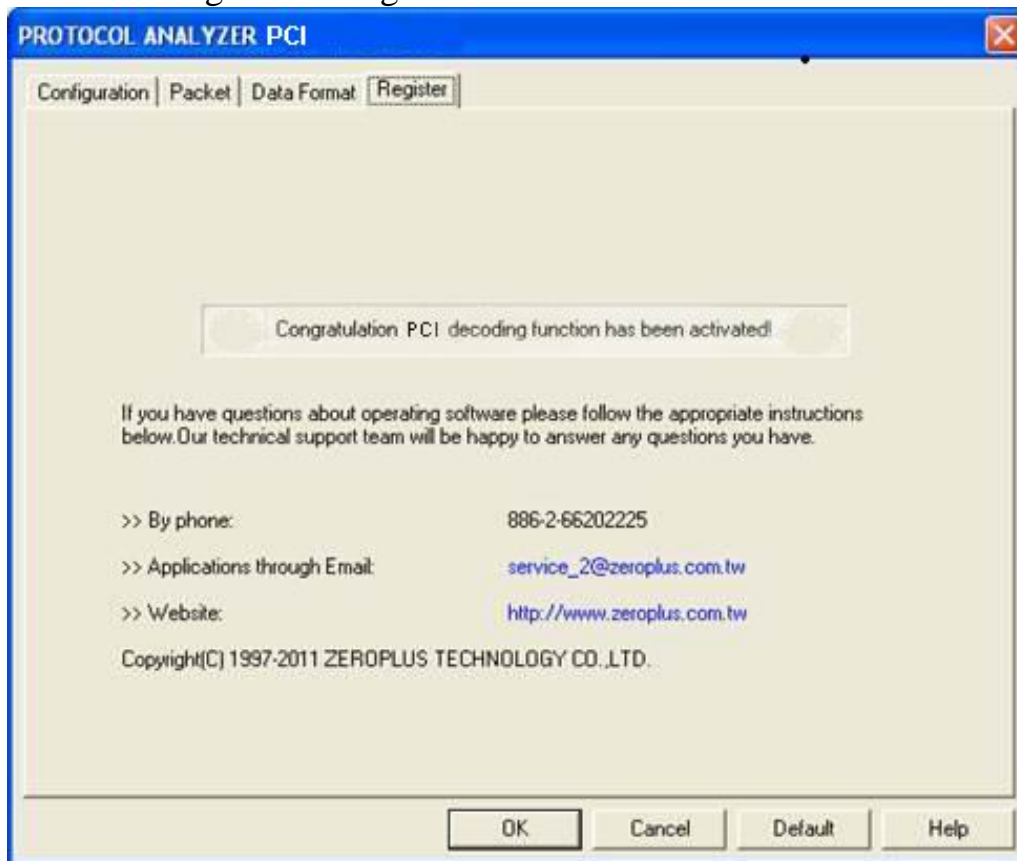
PCI Data Format Dialog Box



Users can set the Data Format of the Command, Address, Byte Enable, Parity and Data as their requirements. When selecting the option, Activate, the data formats are decided by the settings in the Protocol Analyzer; when not selecting the option, Activate, the data formats are decided by the settings in the main program.



PCI Data Register Dialog Box

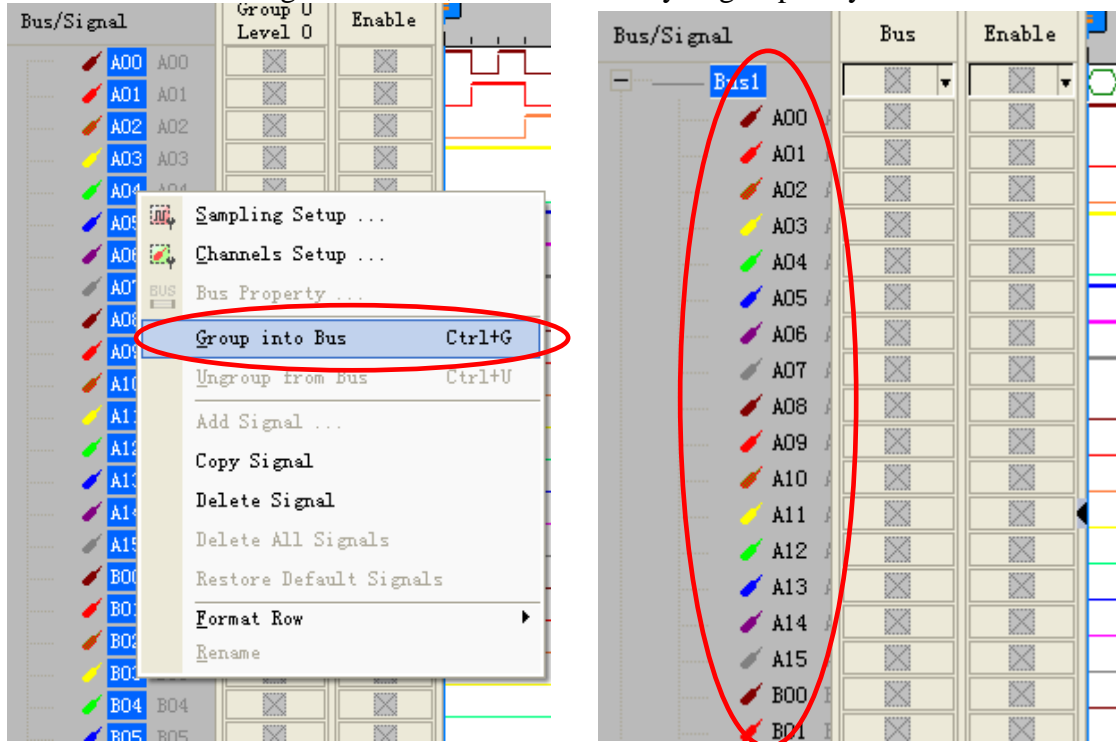


There is written ZeroPlus company information. If you have any questions about software operations, you can contact ZeroPlus by Telephone or Email.

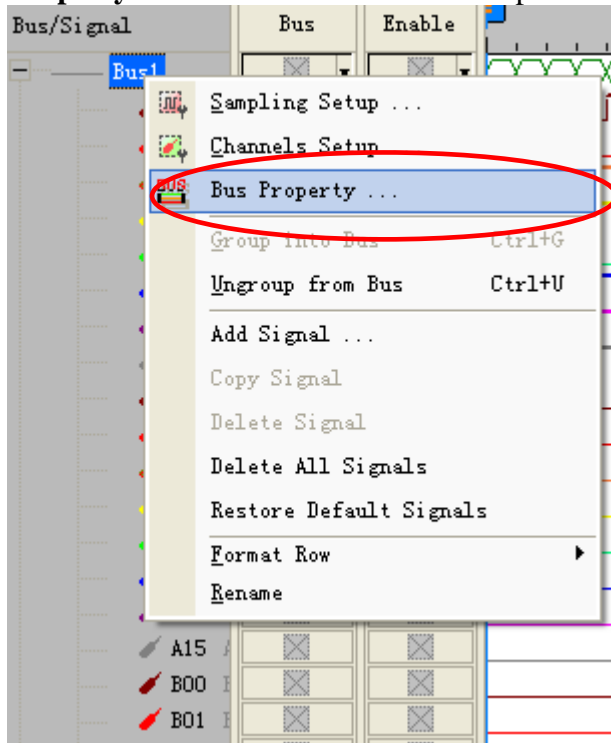


3 Operating Instructions

STEP 1. Group A00-C14 into **Bus1** by pressing the **Right Key** on the mouse. PCI needs forty seven channels to decode signals at least, so it is necessary to group forty-seven or more channels into a Bus.

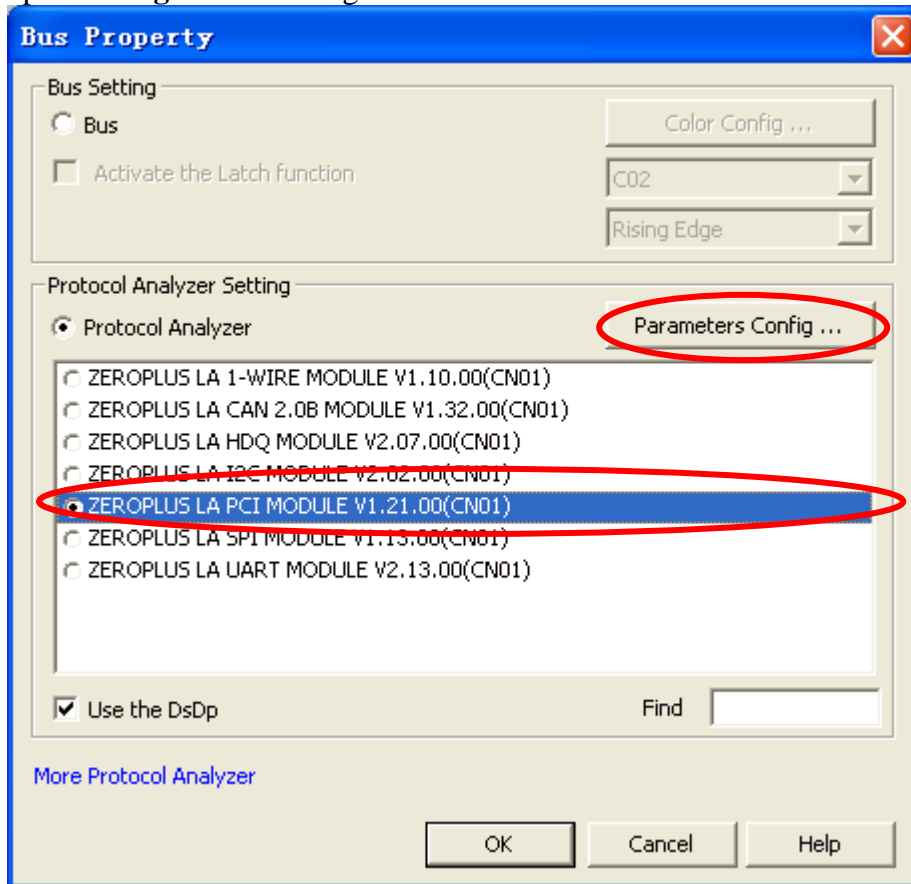


STEP 2. Select **Bus1**, then press **Right Key** on the mouse to list the menu, and then press **Bus Property** or **Bus** bar on the toolbar to open **Bus Property** dialog box.

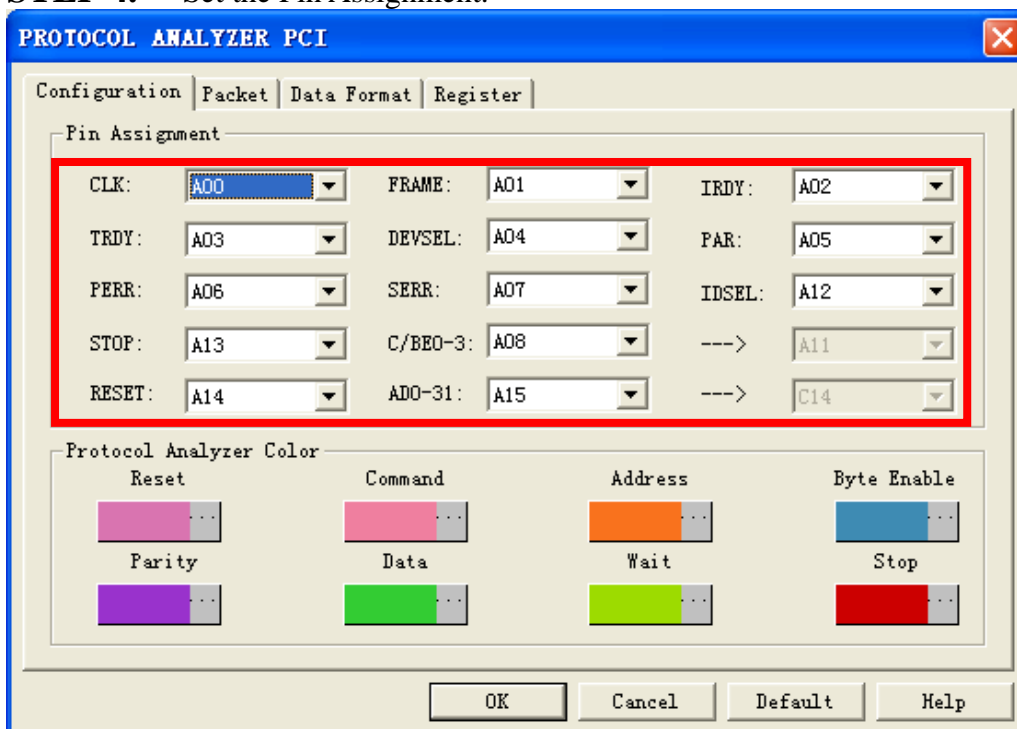




STEP 3. For Protocol Analyzer PCI Parameters Configuration, select Protocol Analyzer, and then select **ZEROPLUS LA PCI MODULE V1.21.01(CN01)**. Next click **Parameters Configuration** to open **Configuration** dialog box.

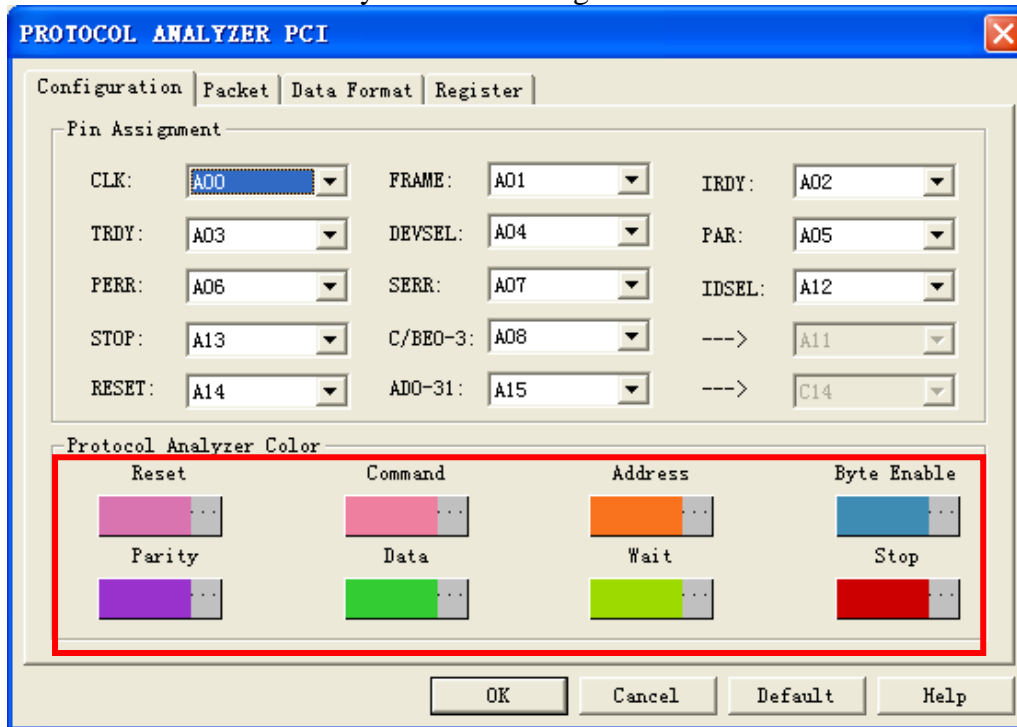


STEP 4. Set the Pin Assignment.



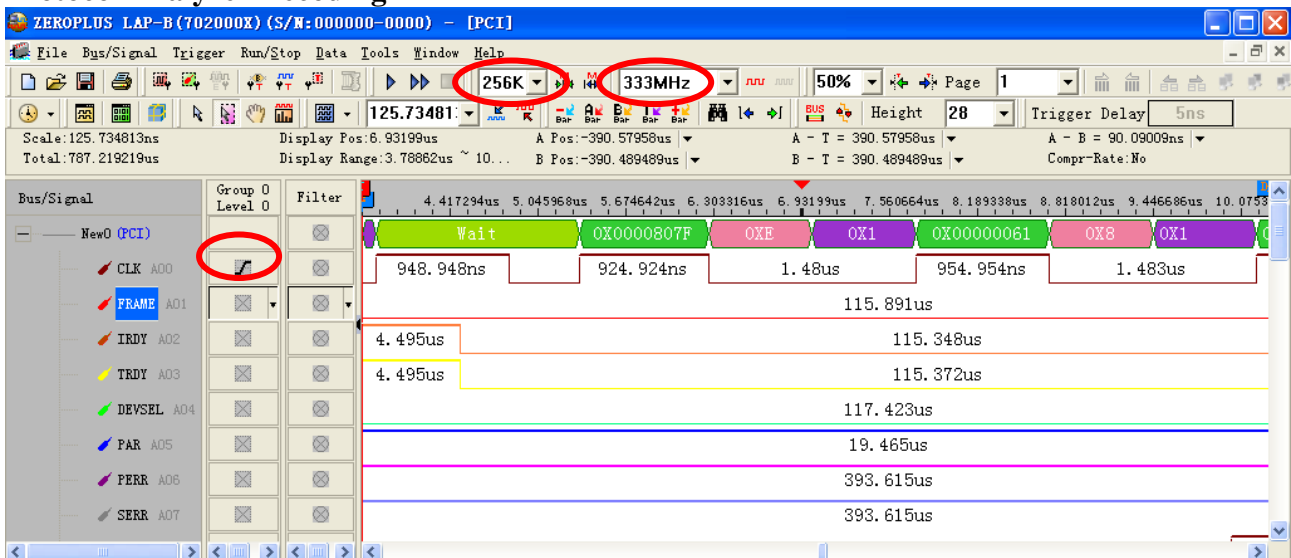


STEP 5. Protocol Analyzer Color settings.



STEP 6. Following pictures show the completion of the protocol analyzer decoding and the packet list. The trigger condition is set as Rising Edge; the memory depth is 256K; the sampling frequency is 333MHz=3ns. (the sampling frequency should be more than 10 times higher than the signal to be tested)

Protocol Analyzer Decoding





Packet List

